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assign Virtual Address

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Patents

Patents 11 - 20 on **assign Virtual Address**. (0.09 seconds)

Method for dynamically remapping a **virtual address** to a physical address to maintain an even ...

US Pat. 6026475 - Filed Nov 26, 1997 - Digital Equipment Corporation

Thus, in determining the optimum cache page **address** to **assign** to the **virtual address** in the private region of **virtual** memory, the private cache page **address** ...

Mechanism for accessing multiple **virtual address** spaces

US Pat. 4521846 - Filed Feb 20, 1981 - International Business Machines Corporation

The GR mask digit for the B field in the X, B, D **address** can be changed when ...
it was assigning base grs to **assign** CR1 to GR5 and to **assign** CR7 to GR12. ...

Method and apparatus for calculating a page table index from a **virtual address**

US Pat. 6393544 - Filed Oct 31, 1999 - Institute for the Development of Emerging Architectures, L.L.C.

Associated with each memory page is a 24-bit region identifier. Therefore, the operating system can **assign** up to 224 individual **virtual address** spaces. ...

Method and system for cache memory congruence class management in a data processing system

US Pat. 5410663 - Filed Oct 15, 1992 - International Business Machines Corporation

YES DETERMINE PREVIOUSLY ASSIGNED **VIRTUAL MEMORY ADDRESS** 1 SELECT NEXT **VIRTUAL MEMORY ADDRESS** WHICH WILL HASH ... **ASSIGN VIRTUAL MEMORY ADDRESS** TO BUFFER 76 ...

Truly anonymous communications using supernets, with the provision of topology hiding

US Pat. 6798782 - Filed Dec 10, 1999 - Sun Microsystems, Inc.

The system of claim 8, wherein the establishing module includes a module to dynamically **assign** a **virtual address** by the independent **address** resolution ...

Content addressable memory with power reduction technique

US Pat. 6785152 - Filed Aug 14, 2003 - Broadcom Corporation

Other embodiments may **assign** one mask bit per **virtual address** bit (if page sizes in multiples of 2 are supported or to provide more granularity) or one mask ...

Content addressable memory with power reduction technique

US Pat. 6646899 - Filed Sep 21, 2001 - Broadcom Corporation

Other embodiments may **assign** one mask bit per **virtual address** bit (if page sizes in multiples of 2 are supported or to provide more granularity) or one mask ...

Methods and systems for enabling communication between a processor and a network operations center

US Pat. 7085854 - Filed Apr 11, 2001 - Corente, Inc.

The network operations center 610 may also **assign** to the gateway 2520 a **virtual address** that is routable in the **virtual** network (step 3435). ...

Memory protection control device and method

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ss7 and "redundant node"

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Patents

Patents 1 - 2 on **ss7 and "redundant node"**. (0.47 seconds)[« View all web results for ss7 and "redundant node"»](#)

Gateway system having a **redundant** structure of media gateway controllers

US Pat. 6944280 - Filed Sep 5, 2001 - NEC Corporation

7 (**SS7**) is used in a carrier network. On the other hand, development has been made... By realizing a **redundant node** structure of media gateway controllers ...

Programmable distributed digital switch system

US Pat. 6091714 - Filed Apr 30, 1997

Communication between the DSP circuits on **redundant node** controllers 32 and 34... carrying LAPD or **SS7** channels. This is facilitated via seven network TDM ...

ss7 and "redundant node"

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EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	24126	(ss7 signaling adj system)	US-PGPUB ; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/27 14:30
L2	5614	(ss7 signaling adj system).bsum.	US-PGPUB ; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/27 14:30
L3	497	(ss7 signaling adj system) same (redunda\$2 fail\$lover availab\$5 rerout\$3) with (node switch)	US-PGPUB ; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/27 14:33
L4	35	3 and (address\$3) with (node switch) with (reassign\$3 transfer\$4 migrat\$3)	US-PGPUB ; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/27 14:35
L5	1	(US-6760343-\$).did.	USPAT	OR	ON	2007/04/27 14:37
L7	1	5 and address\$3	USPAT	OR	ON	2007/04/27 14:42
L8	624	379/221.01-221.04.ccls.	USPAT	OR	ON	2007/04/27 14:43
L9	655	379/221.08-221.12.ccls.	USPAT	OR	ON	2007/04/27 14:44
L10	45	8 and 9	USPAT	OR	ON	2007/04/27 14:44
L11	30	10 and address\$3	USPAT	OR	ON	2007/04/27 14:44